

Refine Search

Search Results -

Terms	Documents
L16 and L17	18

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L18

Search History

DATE: Tuesday, January 20, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> <u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L18</u> 116 and 117	18	<u>L18</u>
<u>L17</u> (flash with ((internal\$3 or local\$3) near4 bus))	287	<u>L17</u>
<u>L16</u> ((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code) and (bus with flash) and ((program or instruction or code) with (power\$3 or startup or start\$3) with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	162	<u>L16</u>
<u>L15</u> ((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code) and (bus with (volatile or RAM or SRAM or DRAM or (random adj2 memory))) and (bus with flash))	810	<u>L15</u>
<u>L14</u> ((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code) and (volatile or RAM or SRAM or DRAM or (random adj2 memory)) near8 (code or (program adj2 instruction)) and flash near8 (code or (program adj2 instruction)) and (bus with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	188	<u>L14</u>

<u>L13</u>	((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code) and (volatile or RAM or SRAM or DRAM or (random adj2 memory))) near8 (code or (program adj2 instruction)) and flash near8 (code or (program adj2 instruction)))	359	<u>L13</u>
<u>L12</u>	((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code))	56444	<u>L12</u>
<u>L11</u>	((volatile or RAM or SRAM or DRAM or (random adj2 memory))) near8 (code or (program adj2 instruction)))	12251	<u>L11</u>
<u>L10</u>	(flash near8 (code or (program adj2 instruction)))	2299	<u>L10</u>
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L9</u>	L8 and l7 and l6	0	<u>L9</u>
<u>L8</u>	(volatile or RAM or SRAM or DRAM or (random adj2 memory)) with (execut\$4 or run\$4) near6 (program or instruction or code)	3106	<u>L8</u>
<u>L7</u>	(processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code)	21195	<u>L7</u>
<u>L6</u>	flash with ((internal\$3 or local\$3) near4 bus)	32	<u>L6</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L5</u>	L4 and l3 and l2	69	<u>L5</u>
<u>L4</u>	(volatile or RAM or SRAM or DRAM or (random adj2 memory)) with (execut\$4 or run\$4) near6 (program or instruction or code)	14930	<u>L4</u>
<u>L3</u>	(processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code)	56444	<u>L3</u>
<u>L2</u>	flash with ((internal\$3 or local\$3) near4 bus)	287	<u>L2</u>
<u>L1</u>	((processor or CPU or host) with (execut\$4 or run\$4) with (program or instruction or code) and (bus with flash) and ((program or instruction or code) with (power\$3 or startup or start\$3) with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	162	<u>L1</u>

END OF SEARCH HISTORY